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[[30]] and the data processing means [(40; 95)] so as to adapt them to the network [(39)], in which the data flow under the control of sequencer means [(8)], [which] said process comprising locking [is one wherein] the sequencer means (8) [are locked] to the rate of the network [(39) so as to synchronize with] and synchronizing the latter with the flow of the data through the adapter means [(86, 96)].

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2. (Amended) The transmission process as claimed in claim 1, [in which] comprising controlling at least one buffer register [(32; 33)], for interfacing with the radio means [(30),] of the adapter means [(86, 96) is controlled] in synchronism with the rate of the network [(39)].

3. (Amended) The transmission process as claimed in claim 2, [in which] comprising controlling data adaptation circuits [(87, 88; 97, 98),] connected to the buffer register [(32; 33),] of the adapter means [are controlled] in synchronism with the rate of the network [(39)].

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4. (Amended) The transmission process as claims [in one of] claim[s] 1 [to 3], [in which] comprising filling a buffer register [(31) is filled] with data to be sent originating from the processing [(40; 95)]; and generating extraction pulses synchronized with the rate of the network [(39) are generated] in a central unit [(10)] of the sequencer means so as to extract the data from the buffer register (31), adapt them by a coding (86) and transmit them (32) to the radio means (30).

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5. (Amended) The process as claimed in [one of claims] claim 1 [to 4], [in which] comprising storing the data originating from the radio means [(30) are stored] in a buffer register (33), and generating extraction pulses synchronized with the rate of the network [(39) are generated] in a central unit [(10)] of the sequencer means so as to extract the data from the buffer register [(33)], adapt them by a decoding [(96)] and transmit them (34,11) to the processing means [(40; 95)].

6. (Amended) A data transmission module for implementing the process of claim 1, comprising radio means [(30)] for interfacing with a radio communication network [(39)] transmitting the data at a specified rate, data adapter means [(86, 96)] arranged so as to be interposed between the radio means [(30)] and data processing means [(40; 95)] and to adapt them to the network [(39)], in which the data flow under the control of sequencer means [(8)], wherein the sequencer means [(8)] and the adapter means [(86, 96)] are grouped into a central unit [(10)] comprising means [(81)] for frequency-locking the sequencer means [(8)] to the rate of the network [(39)].

7. (Amended) The transmission module as claimed in claim 6, in which the frequency-locking means comprise a time base [(8)] regulated by the network [(39)].

8. (Amended) The transmission module as claimed in claim 7, in which the time base comprises frequency dividers [(82)] arranged so as to divide the rate of the network [(39)] and

cyclically control data exchanges between the adapter means [(86; 96)] and the radio means [(30)].

9. (Amended) The transmission module as claimed in claim 8, in which the adapter means [(86; 96)] comprise at least one buffer register [(32; 33)] for exchanging with the radio means [(30)], which is controlled by the frequency dividers [(82)].

10. (Amended) The transmission module as claims in [one of claims] claim 8 [to 9], in which the adapter means [(86; 96)] are connected to at least one buffer register [(31; 34)] for exchanging with the processing means [(40; 95)], which is controlled by the frequency dividers [(82)].

11. (Amended) The transmission module as claimed in [one of claims] claim 8 [to 10], in which the adapter means [(86; 96)] are arranged so as to carry out the adaptation in synchronism with said exchanges with the radio means [(30)].

12. (Amended) The transmission module as claimed in claim 11, in which the sequencer means [(8)] are arranged so as to control in succession a transfer of data from the processing means [(40; 95)] to a send path input buffer register [(31)], from the latter to the adapter means [(86)] and from the latter to the radio means [(30)] through a send-mode output register.

13. (Amended) The transmission module as claimed in [one of claims] claim 11 [and 12], in which the sequencer means [(8)]

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are arranged so as to control in succession a transfer of data from the radio means [(30)] to a receive path input register [(33)], from the latter to the adapter means [(96)] and from the latter to the processing means [(40; 95)] through a receive-mode output register [(34)].

14. (Amended) The transmission module as claimed in [one of claims] claim 6 [to 13], in which the processing circuits [(95)] are incorporated into the module.

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16. (Amended) The mobile terminal as claimed in claim 15, in which the processing means [(95)] are arranged so as to process data exchanged with the [INTERNET] internet network.

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17. (Amended) The mobile radio telephone terminal incorporating the module as claimed in [one of claims] claim 6 [to 13].

Please add the following new claims:

sub 7
18. The mobile radio telephone terminal incorporating the module as claimed in claim 7.

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19. The mobile radio telephone terminal incorporating the module as claimed in claim 8.

20. The mobile radio telephone terminal incorporating the module as claimed in claim 9.